# **N-Channel Power MOSFET** 600 V, 8.0 $\Omega$

#### **Features**

- 100% Avalanche Tested
- Extremely High dv/dt Capability
- Gate Charge Minimized
- Zener-protected
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS

#### ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub> 600		V
Gate-to-Source Voltage	V <sub>GS</sub>	±30	V
Continuous Drain Current $R_{\theta JA}$ Steady State, $T_C = 25^{\circ}C$	Ι <sub>D</sub>	0.3	Α
Continuous Drain Current $R_{\theta JA}$ Steady State, $T_C = 100^{\circ}C$	Ι <sub>D</sub>	0.21	Α
	P <sub>D</sub>	2.0	W
Pulsed Drain Current	I <sub>DM</sub>	5	Α
Continuous Source Current (Body Diode)	I <sub>S</sub>	2.2	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>D</sub> = 1.4 A)	EAS	38	mJ
Peak Diode Recovery (Note 1)	dV/dt	4.5	V/ns
Maximum Temperature for Soldering Leads	TL	260	°C
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>STG</sub>	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1.  $I_S < 2.2 \text{ A}$ ,  $di/dt \le 200 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le BV_{DSS}$ ,  $T_J = +150^{\circ}\text{C}$ 

#### THERMAL RESISTANCE

Parameter	Symbol	Value	Unit
Junction-to-Ambient Steady State NDT02N60Z (Note 2) NDT02N60Z (Note 3)	$R_{\theta JA}$	61 148	°C/W

- 2. Surface mounted on FR4 board using 1" sq. pad size
- (Cu area = 1.127" sq. [2 oz] including traces)

  3. Surface–mounted on FR4 board using minimum recommended pad size (Cu area = 0.026" sq. [2 oz]).

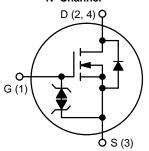


### ON Semiconductor®

#### http://onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX		
600 V	8.0 Ω @ 10 V		

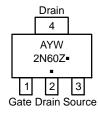
#### **N-Channel**



#### **MARKING DIAGRAM**



SOT-223 CASE 318E STYLE 3



= Assembly Location

= Year W = Work Week

2N60Z = Specific Device Code = Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	s	Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA		600			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	Reference to 25°C, I <sub>D</sub> = 1 mA			605		mV/°C
Drain-to-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	$T_J = 25^{\circ}C$			1	μΑ
			T <sub>J</sub> = 125°C			50	1
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20 V				±10	μΑ
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{DS} = V_{GS}$ , $I_D = 50$	) μΑ	3.0	3.9	4.5	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>	Reference to 25°C, I <sub>D</sub>	= 50 μΑ		10.2		mV/°C
Static Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 0$	.7 A		5.9	8.0	Ω
Forward Transconductance	9FS	$V_{DS} = 15 \text{ V}, I_{D} = 0$	.7 A		1.3		S
DYNAMIC CHARACTERISTICS							
Input Capacitance (Note 5)	C <sub>iss</sub>				170		pF
Output Capacitance (Note 5)	C <sub>oss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz			22		1
Reverse Transfer Capacitance (Note 5)	C <sub>rss</sub>		•		4.8		1
Effective output capacitance, energy related (Note 7)	C <sub>o(er)</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 0 \text{ to } 480 \text{ V}$			7.8		]
Effective output capacitance, time related (Note 8)	C <sub>o(tr)</sub>	$I_D$ = constant, $V_{GS}$ = 0 V, $V_{DS}$ = 0 to 480 V			12.4		1
Total Gate Charge (Note 5)	$Q_g$	V <sub>DS</sub> = 300 V, I <sub>D</sub> = 1.6 A, V <sub>GS</sub> = 10 V			7.4		nC
Gate-to-Source Charge (Note 5)	Q <sub>gs</sub>				1.8		1
Gate-to-Drain ("Miller") Charge (Note 5)	Q <sub>gd</sub>				3.8		1
Plateau Voltage	V <sub>GP</sub>				6.4		V
Gate Resistance	Rg				11.5		Ω
RESISTIVE SWITCHING CHARACTERIS	TICS (Note 6)						
Turn-on Delay Time	t <sub>d(on)</sub>				10		ns
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 1	.6 A,		6		1
Turn-off Delay Time	t <sub>d(off)</sub>	$V_{DD} = 300 \text{ V, } I_{D} = 1.6 \text{ A,}$ $V_{GS} = 10 \text{ V, } R_{G} = 0 \Omega$			14		1
Fall Time	t <sub>f</sub>				8		1
SOURCE-DRAIN DIODE CHARACTERIS	STICS						
Diode Forward Voltage	$V_{SD}$	$I_S = 1.6 \text{ A}, V_{GS} = 0 \text{ V}$ $T_J = 25^{\circ}\text{C}$ $T_J = 100^{\circ}\text{C}$	T <sub>J</sub> = 25°C		0.9	1.2	V
				0.8		1	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0 \text{ V}, V_{DD} = 30 \text{ V}, I_{S} = 1.6 \text{ A},$ $d_i/d_t = 100 \text{ A/}\mu\text{s}$			230		ns
Charge Time	ta				50		1
Discharge Time	t <sub>b</sub>				180		1
Reverse Recovery Charge	Q <sub>rr</sub>				495		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 4. Pulse Width ≤ 380 μs, Duty Cycle ≤ 2%.
- 5. Guaranteed by design.
- Switching characteristics are independent of operating junction temperatures.
   C<sub>o(er)</sub> is a fixed capacitance that gives the same stored energy as C<sub>oss</sub> while V<sub>DS</sub> is rising from 0 to 80% V<sub>(BR)DSS</sub>
   C<sub>o(tr)</sub> is a fixed capacitance that gives the same charging time as C<sub>oss</sub> while V<sub>DS</sub> is rising from 0 to 80% V<sub>(BR)DSS</sub>

#### TYPICAL CHARACTERISTICS

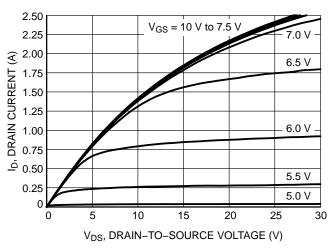


Figure 1. On-Region Characteristics

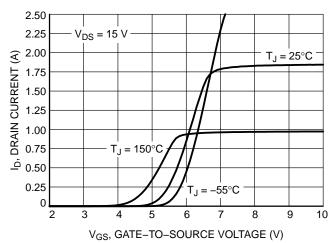


Figure 2. Transfer Characteristics

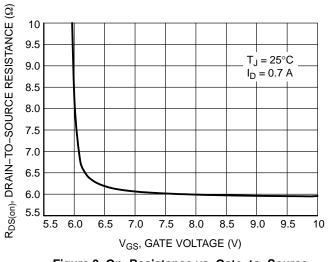


Figure 3. On-Resistance vs. Gate-to-Source Voltage

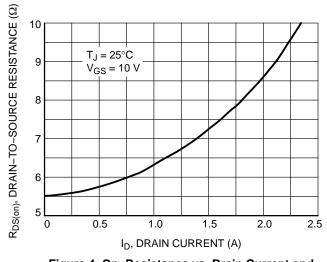


Figure 4. On–Resistance vs. Drain Current and Gate Voltage

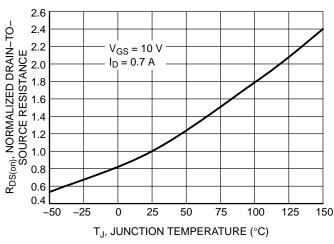


Figure 5. On–Resistance Variation with Temperature

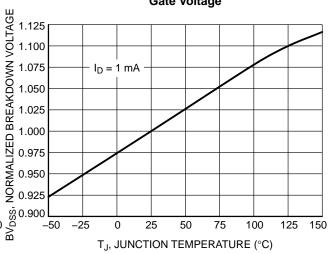


Figure 6. Breakdown Voltage Variation with Temperature

#### TYPICAL CHARACTERISTICS

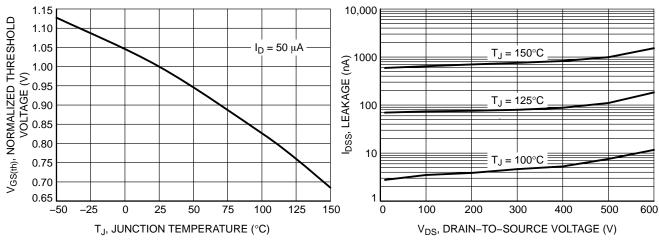


Figure 7. Threshold Voltage Variation with Temperature

Figure 8. Drain-to-Source Leakage Current vs. Voltage

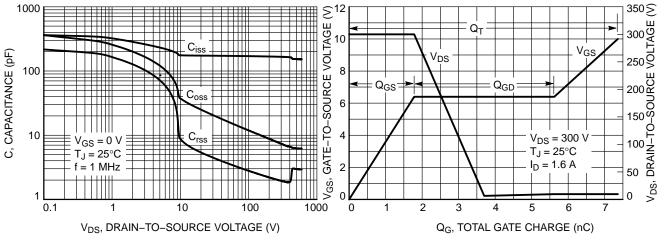


Figure 9. Capacitance Variation

Figure 10. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

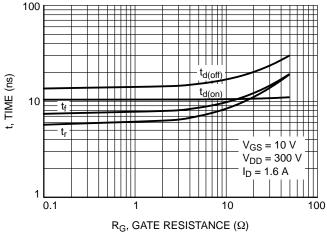


Figure 11. Resistive Switching Time Variation vs. Gate Resistance

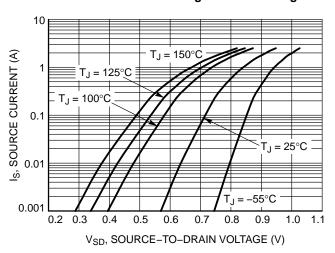


Figure 12. Diode Forward Voltage vs. Current

#### TYPICAL CHARACTERISTICS

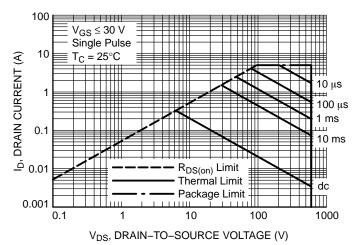


Figure 13. Maximum Rated Forward Biased Safe Operating Area

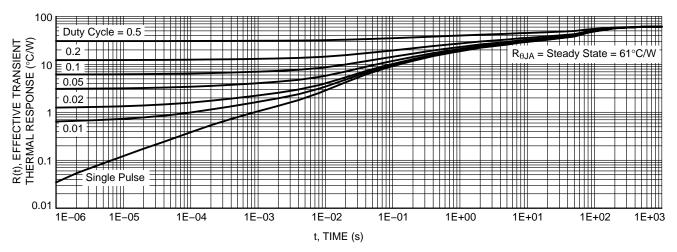


Figure 14. Thermal Impedance (Junction-to-Ambient)

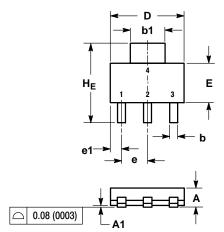
#### **ORDERING INFORMATION**

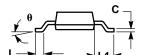
Device	Package	Shipping <sup>†</sup>
NDT02N60ZT1G	SOT-223	1000 / Tape & Reel
NDT02N60ZT3G	(Pb-Free, Halogen Free)	4000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

SOT-223 (TO-261) CASE 318E-04 ISSUE N





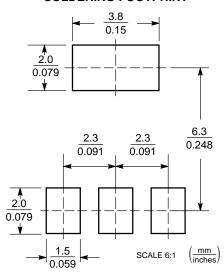
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: INCH.

	MILLIMETERS				INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
С	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
е	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L	0.20			0.008	-	
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	-	10°	0°	_	10°

STYLE 3: PIN 1. GATE

2. DRAIN SOURCE 4. DRAIN

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